

**Jharkhand University of
Technology Jharkhand, Ranchi**
Proposed Syllabus for B.Tech 3rd Semester

Electronics and Communication Engineering

Electronics and Communication Engineering3rd semester course structure

Sl. No.	Course Code	Subject	L	T	P	Credit
01	EC301	Basic Electronics	3	1	0	3
02	EC302	Digital Electronics And Logic Design	3	1	0	3
03	EE302	Network Theory	3	1	0	3
04	EE303	Electromagnetic Field Theory	3	1	0	3
05	BSC301	Mathematics-III	3	1	0	4
06	BSC302	Environmental Science	2	0	0	0
01	EC301P	Basic Electronics Lab	0	0	3	1
02	EC302P	Digital Electronics And Logic Design Lab	0	0	3	1
03	EE302P	Network Theory Lab	0	0	3	1
04	EX301	Extra Activities (NSO/NSS/NCC/Yoga / Creative Arts/Mini Project)	0	0	2	1
05	HS301	Communication Skill Lab	0	0	2	1
Total credit						21

PROPOSED SYLLABUS FOR ALL BRANCHES EXCEPT CSE & IT

BSC301 MATHEMATICS III

Module -1

Laplace Transformation: Laplace Transformation and its properties, Periodic function, Unit step function and impulse function .Inverse Laplace Transformation, Convolution Theorem, Applications of Laplace transforms in solving certain initial value problems & simultaneous differential equations. **(8L/1.5Q)**

Module-2

Numerical Method: Finite difference, Symbolic relations, Interpolation and Extrapolation, Newton - Gregory forward and backward formula, Lagrange's formula, Inverse Interpolation by Lagrange's formula. Numerical Differentiation and Numerical Integration, Newton Cotes Quadrature formula, Trapezoidal rule. Simpson's 1/3" rule, Simpson's 3/8" rule. **(10L/1.5Q)**

Module -3

Z-Transform & Inverse Z-Transform- Properties - Initial and Final value theorems, Convolution theorem- Difference equations. Solution of difference equations using Z-Transformation. **(6L/1.5Q)**

Module -IV

Fourier Series & Fourier Transform: Expansion of - Algebraic, Exponential & Trigonometric functions in Fourier series, Change of interval, Even and odd function, half range sine and cosine series, Complex form of Fourier series.

Fourier Transformation and inverse Fourier Transformation, Fourier sine & cosine transforms.

Convolution theorem for Fourier transforms with simple illustrations. **(8L/1.5Q)**

Module V

Partial Differential Equations: Formation of partial differential equations, Linear partial differential equations of first order, Lagrange's linear equation, Non-linear equations of first order, Charpit's method Solution of one dimensional Wave equation & Heat equation by the method of separation of variables and its applications. **(8L/1Q)**

Note-Question no.1 will be compulsory, objective type with 7 sub-parts comprising of the whole syllabus.

Text Books

1. Irwin Kreyszig, Advanced Engineering Mathematics, 9th Edition, John Wiley & Sons,
2. Ramana R. V ., Higher Engineering Mathematics, Tata McGraw Hill New Delhi,2010.
3. B.S. Grewal, Higher Engineering Mathematics, Khanna Publishers, 44th Edition,

Reference Books

1. R. J. Beerends .H. G. Ter Morsche, J. C. Van Den Berg. L. M. Van De Vrie, Fourier and Laplace Transforms, Cambridge University Press.
2. Sastry S.S. Introductory Methods of Numerical Analysis, PHI

BASIC ELECTRONICS
(ECE, EEE, EE, CSE, IT) Course
code -EC 301
L T P CR.

3 10 3

Module I: Basic Electronic Components

Active and Passive Components, Types of resistors and Colour coding, Capacitors, Inductors applications of Resistor, Capacitor and Inductor, Relay, LDR, Basic Integrated Circuits (IC 7805, 7809, 7812, 555 etc.). Measuring Instruments like CRO, Power supply, multi-meters etc.

Module II: Semiconductors

Difference between Insulators, Semiconductors and Conductors, Mobility and Conductivity, Intrinsic and Extrinsic Semiconductors, Fermi Level, Energy band, Charge Densities in Semiconductors, Mass Action Law, Current Components in Semiconductors, Drift and Diffusion Current, The Continuity Equation, Injected Minority Charge Carrier, Hall Effect, P-N Junction Diode, construction, working, characteristics and diode equation Application of Diode, Rectifier: Half Wave, Full Wave and Bridge Rectifier, Zener Diode and its Applications, Varactor Diode, Schottky Diode, Regulated Power Supply using Zener Diode and Regulated ICs, LED, Photodetector.

Module III: Transistors

Construction, Working, Modes and Configuration of BJT, Input and Output Characteristics of all Configurations, Comparison of all Configuration & Modes, BJT as a Switch and as an Amplifier. h -parameter, JFET Construction, working and characteristics. MOSFET Construction, working and Characteristics, Types of MOSFET.

Module IV: Power electronic devices & Communication engineering

Construction, characteristics and working of SCR, DIAC, TRIAC and UJT. Introduction, Characteristics and applications of Operational Amplifier (IC741). Modulation and its types.

Module V: Digital Logic and basic circuit Design

Number systems and conversion (DECIMAL, OCTAL, HEXADECIMAL, BINARY, BCD etc.), binary addition and subtraction, Logic Gates and their truth-table, Boolean algebra. Design of Single Stage Amplifier, LED Driver Circuit, Infrared Transmitter Receiver Circuit, LDR Driver Circuit, Relay Driver Circuit, Square Wave and Fix Frequency Generator using 555 IC.

Text Books

1. Basic Electronics and Linear Circuits by N. N. Bhargava, D. C. Kulshreshtha and S. C. Gupta, TMH Publications.
2. Op-Amps and Linear Integrated Circuits by Ramakant A. Gayakwad, PHI Publications.
3. Electronic Devices and Circuits by Godse and Bakshi Technical, Vol-1 Technical Publication Pune.

Reference Books

1. Integrated Devices & Circuits by Millman & Halkias, TMH Publications.
2. Electronics Devices and Circuit Theory by R. Boylestad & L. Nashelsky, Pearson Publication
3. Electronic Communication System by G. Kennedy, TMH Publications.
4. Basic Electronics by Sanjeev Kumar & Vandana Sachdeva, Paragaon International Publication

DIGITAL ELECTRONICS AND LOGIC DESIGN

(ECE, CSE, IT)

Course code -EC 302

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Module I: Binary Codes and Boolean algebra

Analog and Digital, Binary Number System. Addition, Subtraction, Multiplication, Division of binary numbers, Subtraction using 2's complement method. Binary codes: weighted and non-weighted codes, self-complementary codes, BCD, Excess-3, Gray codes, Alphanumeric codes, ASCII Codes. *Boolean algebra:*

Boolean Laws and Expression using Logic Gates, Realization of different gates using Universal gates, DeMorgan's Theorem, Duality Theorems.

Module II: Boolean function minimization Techniques

Standard forms: SOP, POS, Simplification of Switching function & representation

(Maxterm & Minterm), Boolean expression & representation using logic gates, Propagation delay in logic gate. *Karnaugh map:* K-map(up to 5 variables), mapping and minimization of SOP and

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POS expression, Don't care condition, conversion from SOP to POS and POS to SOP form using K-map, Minimization of multiple output circuits, Quine Mc-cluskey method minimization technique, prime implicant table, Don't care condition.

Module III: Combinational Circuits Design

Adder & Subtractor (Half and Full), Parallel Binary adder, BCD Adder, Binary multipliers, Code Converters, parity bit generator, Comparators, Decoder, BCD to 7-segment Decoder, Encoders, Priority Encoders, Multiplexers, De Multiplexers.

Module IV: Sequential Circuits Elements

Introduction to sequential circuit, Flip-flop & Timing Circuits: SR latch, Gated latch, Tri state logic, Edge triggered flip-flop: - D, JK, T Flip-flop, flip-flop asynchronous inputs, characteristic table of Flip-flop, excitation table of Flip-flop, master slave JK flip flop, inter conversion of Flip-flop. Study of timing parameters of flip-flop. Shift registers: buffer register, controlled buffer register. Data transmission in shift register SISO, SIPO, PISO, PIPO, Bidirectional shift register, universal shift registers. *Counter*: Classification, Ripple or asynchronous counter, Effect of propagation delay in ripple counters, up-down counter, Design of Mod-n counter, synchronous counter, Ring counter, Johnson counter. Introduction to FSM. Design of synchronous FSM, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator.

Module V: Logic Families and VLSI Design Flow

Logic Families and Semiconductor Memories: TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing, Memory elements, Concept of Programmable logic devices like FPGA, Logic implementation using Programmable Devices VLSI Design flow: Design entry, Schematic, FSM & HDL, different modelling styles in

VHDL, Data types and objects, Dataflow, Behavioural and Structural Modelling, Synthesis and Simulation VHDL constructs and codes for combinational and sequential circuits

Text Books:

1. Kharate "Digital Electronics" OXFORD Publication
2. A. Anand Kumar 'Fundamentals of Digital Circuits'. PHI Publications
3. R.P. Jain-'Modern Digital Electronics' IIIrd Edition- Tata Mc Graw Hill, Publication
4. Douglas Perry, "VHDL", Tata McGraw Hill, 4th edition, 2002.
5. Charles Roth, "Digital System Design using VHDL", Tata McGraw Hill 2nd edition
6. Bhaskar VHDL BASED DESIGN, PEARSON EDUCATION

Reference Books:

1. Rajkamal 'Digital Systems Principals and Design' Pearson Education
 2. A.P. Malvino, D.P. Leach 'Digital Principles & Applicatios' -VIth Edition-TMH publication.
 3. M. Morris Mano 'Digital Design' (Third Edition). PHI Publications
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NETWORK THEORY**(ECE, EEE, EE)****Course code -EE 302****L T P CR.****3 1 0 3****Module I: Circuit Fundamentals**

Voltage sources, Current sources, Conversion of voltage sources to current sources and vice a versa. Network terminology :- Node, Junction, Branch, Loop, Network solution by branch current method, Loop or Mesh current method, Node voltage method, Star delta connection and conversion. Node and Mesh Analysis, matrix approach of network containing voltage and current sources, and reactance's, source transformation and duality. Network theorems: Superposition, reciprocity, Thevenin's, Norton's, Maximum power Transfer, compensation and Tallegen's theorem as applied to AC, circuits. Trigonometric and exponential Fourier series: Discrete spectra and symmetry of waveform, steady state response of a network to non-sinusoidal periodic inputs, power factor, effective values, Fourier transform and continuous spectra, three phase unbalanced circuit and power calculation.

Module II: Resonance Circuits

Series resonance circuit, Frequency response of a series resonant circuit, Q factor, Bandwidth, selectivity, Effect of Q on bandwidth and selectivity, Relation between bandwidth and Q, Impedance of a series resonant circuit, Resonance by variation of L and C, Parallel resonant circuit and effect of resistance of a capacitance, Frequency response of parallel resonant circuit.

Module III: Two- Port Network

Two- port network parameters, r, y, z, h, A B C D relation between the parameters, Inter-conversion of two port networks, cascade connection series connection, series parallel connection, T and M network representation of a two port network.

Module IV: Network Functions

Laplace transform, transform of a voltage and current, Transform of circuit elements, Network functions, Poles and zeros of the network functions, Pole zero plot, Physical significance of poles and zeroes, Stability, Two-port network parameters in the frequency domain Transient response: - step input response in RL circuit, step input response in R-C circuit, step input response in R-L-C circuit, ac transients.

Module V: Filters and Attenuators

Definitions, classification and characteristics of different filters, filter fundamentals such as attenuation constant(α), phase shift (β), propagation constant (γ), characteristic impedance (Z_0), decibel, neper. Design and analysis of constant K, M derived and composite filters (low pass, high pass, band pass, and band stop filters): T and PI sections. Definitions, classification, relation between neper and decibel, analysis and design of T type, PI type, alpha lattice, bridged – T and L types attenuators.

Text Books:

1. "A.Sudhakar, Shymmohan S. Palli, Circuit and Network – Analysis and Synthesis’, 3 rd Edition, Tata McGraw Hill Publication.
2. Van, Valkenburg; “Network analysis”; Prentice Hall of India, 2000.
3. A. Chakrabarti, Circuit theory (Analysis and Synthesis) ‘, IIIrd edition, Dhanpat Rai and Co.

Reference Books:

1. D. Roy Choudhuri, Networks and Systems’, New Age International Publisher.
 2. M.E.Van Valkenburg Network Analysis’, IIIrd edition, Pearsons Education/PHI.
 3. Joseph Edministrar, Theory and Problems of Electronic Circuit (Schaum’s Series) – Tata McGraw Hill Publication.
 4. Soni Gupta, Electrical Circuit Analysis’, Dhanpat Rai and Co.
 5. Boylestad, Introductory Circuit Analysis’, Universal Book Stall, New
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ELECTROMAGNETIC FIELD THEORY

(ECE, EEE, EE)

Course code -EE 303

L T P CR.

3 1 0 3

Module I: Coordinate Systems and Transformation:

Basics of Vectors: Addition, subtraction and multiplications; Cartesian, Cylindrical, Spherical transformation. Vector calculus: Differential length, area and volume, line surface and volume integrals, Del operator, Gradient, Divergence of a vector, Divergence theorem, Curl of a vector, Stokes's theorem, Laplacian of a scalar.

Module II: Electrostatic fields:

Coulombs law and field intensity, Electric field due to charge distribution, Electric flux density, Gauss's Law- Maxwell's equation, Electric dipole and flux line, Energy density in electrostatic fields, Electric field in material space: Properties of materials, convection and conduction currents, conductors, polarization in dielectrics, Dielectric-constants, Continuity equation and relaxation time, boundary conditions, Electrostatic boundary value problems: Poisson's and Laplace's equations., Methods of Images.

Module III: Magneto Statics:

Magneto-static fields, Biot - Savart's Law, Ampere's circuit law, Maxwell's equation, Application of ampere's law, Magnetic flux density- Maxwell's equation, Maxwell's equation for static fields, magnetic scalar and vector potential.

Module IV: Magnetic Forces:

Materials and devices, Forces due to magnetic field, Magnetic torque and moment, a magnetic dipole. Magnetization in materials, Magnetic boundary conditions, Inductors and inductances, Magnetic energy.

Module V: Waves and Applications:

Maxwell's equation, Faraday's Law, transformer and motional electromotive forces, Displacement current, Maxwell's equation in final form Electromagnetic wave propagation: Wave propagation in loss dielectrics, Plane waves in lossless dielectrics Plane wave in free space. Plain waves in good conductors, Power and the pointing vector, Reflection of a plain wave in a normal incidence. Transmission Lines, and Smith Chart.

Text Book:

1. MNO Sadiku, "Elements of Electromagnetic", Oxford University Press.

Reference Books:

1. WH Hayt and JA Buck, "Engineering Electromagnetic", McGraw- Hill Education.
2. Antenna and wave propagation by K.D Parsad, Satya Prakashan.

ENVIRONMENTALSCIENCE

Course code –BSC 302

L T P CR.

2 0 0 0

(COMMON FOR ALL BRANCH)

Module-1

Concept and scope of Environment science, components of environment, environmental segment and their importance. **(2 Hrs)**

Module-II

Ecology: Ecosystem and its characteristics features, structure and function of forest ecosystem, grassland ecosystem, desert ecosystem and aquatic ecosystem, ecological balance and consequences of imbalance. **(4 Hrs)**

Module-III

Atmosphere: Atmospheric composition, energy balance, climate, weather, depletion of ozone layer, greenhouse effect, acid rain, particles, ions and radicals in the atmosphere, chemical and photochemical reactions in the atmosphere.

Module-IV

(4 Hrs)

Air pollution and control: Air pollutants, sources and effect of air pollutants, primary and secondary pollutants, photochemical smog, fly ash, inorganic and organic particulate matter. Air quality standards, sampling, monitoring and control measures for pollutants.

(4 Hrs)

Module-V

Water pollution and control: Aquatic environment, water pollution, sources and their effect, lake and ground water pollution, eutrophication, water quality standard and water pollution control measures, waste water treatment.

Module-VI**(4 Hrs)**

Land pollution; Lithosphere, composition of soil, acid base and ion exchange reactions in soil, soil erosion, landslides, desertification, pollutants (municipal, industrial, commercial, agricultural, hazardous solid wastes), origin and effects, collection and disposal of solid wastes, recovery and conversion methods. **(5 Hrs)**

Module-VII

Noise pollution; Noise classification and its sources, effects and measurement, noise pollution hazards, standards and noise pollution control. **(2 Hrs)**

Books and References:

1. Master, G.M Introduction to environment engineering and science, Pearson Education.
 2. Nebel, B.J., Environment science, Prentice Hall Inc.
 3. Odum, E.P. Ecology: The link between the natural and social sciences. IBH Publishing Company Delhi
 4. De, A.K. Environmental Chemistry, Merrut.
 5. Sharma B.K Environmental Chemistry, Krishna Prakashan Media Merrut.
 6. Kaushik, A and Kaushik, C.P. Perspectives in Environmental studies, New Age International Publication.
 7. Menon, S.E. Environmental Chemistry.
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BASIC ELECTRONICS LAB**(ECE, EEE, EE)****Course code -EC 301P****List of Experiments (Minimum 10)**

1. Identification and testing of Resistors, Inductors, Capacitors, PN-Diode. Zener Diode, LED, LCD, LDR, BJT, Photo Diode, Photo Transistor,
2. Measurement of voltage and current using multimeter, Measure the frequency and Amplitude of a signal with the help of CRO and function generator.
3. Study of p-n junction diode AND Zener Diode I-V characteristics
4. Assemble the single-phase half wave and full wave bridge rectifier & the analyse effect of capacitor as a filter (only study of waveforms).
5. Study of Zener diode as voltage regulator.

6. Measurement & study of input characteristics of a BJT in CB configuration.
7. Measurement and study of characteristics of JFET and MOSFET
8. To design and simulate IR Transmitter and Receiver Circuit.
9. To design and simulate Motor Driver using Relay.
10. To design and simulate Light detector using LDR.
11. To design and simulate Constant frequency square wave generator using.
12. To design and simulate 5 volt DC power supply from 230 AC.

NOTE: At least ten experiments are to be performed, minimum seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus.

DIGITAL ELECTRONICS AND LOGIC DESIGN LAB

(ECE, CSE, IT)

Course code EC 302P

List of Experiments (Minimum 10)

1. Study of TTL gates – AND, OR, NOT, NAND, NOR, EX-OR, EX-NOR.
2. Design & realize a given function using K-maps and verify its performance.
3. To verify the operation of multiplexer & Demultiplexer.
4. To verify the operation of comparator.
5. To verify the truth tables of S-R, J-K, T & D type flip flops.
6. To verify the operation of bi-directional shift register.
7. To design & verify the operation of 3-bit synchronous counter.
8. Design all gates using VHDL.
9. Design a multiplexer using VHDL
10. Design a decoder using VHDL
11. Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. half adder b. full adder
12. Write VHDL programs for the following circuits, check the wave forms and the hardware generated a. multiplexer b. demultiplexer

NOTE: At least ten experiments are to be performed, minimum seven experiments should be performed from above list. Remaining three experiments may either be performed from the above list or designed & set by the concerned institution as per the scope of the syllabus. For VHDL Xilinx software may be used.

NETWORK THEORY LAB

(ECE, EEE, EE)

Course code -EE 302P List of

Experiments (Minimum10)

1. Transient response of RC circuit.
 2. Transient response of RL circuit.
 3. To find the resonance frequency, Band width of RLC series circuit.
 4. To study and verify effect of R on frequency response of parallel resonance circuit.
 5. To calculate and verify "Z" parameters of a two port network.
 6. To calculate and verify "Y" parameters of a two port network.
 7. To determine equivalent parameter of parallel connections of two port network.
 8. To plot the frequency response of low pass filter and determine half-power frequency.
 9. To plot the frequency response of high pass filters and determines the half-power frequency.
 10. To plot the frequency response of band-pass filters and determines the band-width.
 11. To calculate and verify "ABCD" parameters of a two port network.
 12. To synthesize a network of a given network function and verify its response.
 13. Introduction of P-Spice or other simulation software
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COMMUNICATIONSKILL LAB

Course code HS301

This lab paper involves interactive practice sessions in Language Lab along with some class lectures to enable the students to be confident enough in language and professional sphere of life.

Module I: Listening Comprehension

To comprehend spoken material in standard Indian English/ British English & American English

- Current situation in India regarding English
- American English Vs. British English

Module II: Phonetics & Phonology

- Introduction to Phonetics & Phonology
- Organs of Speech/ Speech Mechanism
- Pronunciation, Intonation, Stress and Rhythm, Syllable division
- Consonants/Vowels/Diphthongs Classification

Module III: Common Everyday Situations: Conversations and Dialogues**Module IV: Communication at Workplace****Module V: Telephonic Conversation**

- Introduction
- Listening/Speaking
- Telephonic Skills Required
- Problems of Telephonic Conversation
- Intensive Listening

Module VI: Interviews

- The Interview Process
- Purpose/Planning/Two-way Interaction/Informality
- Pre-interview Preparation Techniques
- Projecting a Positive Image
- Answering strategies

Module VII: Formal Presentations

- Introduction
- Nature/Importance of Presentation
- Planning
- Objective with central idea, main ideas, role of supporting materials
- Handling Stage Fright

Module VIII: Forms of Technical Communication: Technical Report: Definition & importance; Thesis/Project writing: structure & importance; synopsis writing: Methods; Technical research Paper writing: Methods & style; Seminar & Conference paper writing; Expert Technical Lecture: Theme clarity; Analysis & Findings; C.V./Resume writing; Technical Proposal: Types, Structure & Draft.

Module IX: Technical Presentation: Strategies & Techniques Presentation: Forms; interpersonal Communication; Class room presentation; style; method; Individual conferencing: essentials: Public Speaking: method; Techniques: Clarity of substance; emotion; Humour; Modes of Presentation; Overcoming Stage Fear; Audience Analysis &

retention of audience interest; Methods of Presentation: Interpersonal; Impersonal; Audience Participation: Quizzes & Interjections.

Module X: Technical Communication Skills: Interview skills; Group Discussion: Objective & Method; Seminar/Conferences Presentation skills: Focus; Content; Style; Argumentation skills: Devices: Analysis; Cohesion & Emphasis; Critical thinking; Nuances: Exposition narration & Description; effective business communication competence: Grammatical; Discourse competence: combination of expression & conclusion; Socio-linguistic competence: Strategic competence: Solution of communication problems with verbal and non verbal means.
